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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/326,059 12/23/2002 PAT 6,762,106

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-099009 04/01/2002

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/29/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	JAPAN	18	6	1

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## TITLE

Semiconductor device and method for fabricating the same

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